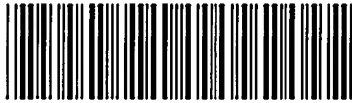


**Search Notes**

Application/Control No.

10/769,202

Examiner

Paul Contino

Applicant(s)/Patent under  
Reexamination

DUBE, JEAN-FRANCOIS

Art Unit

2114

**SEARCHED**

Class	Subclass	Date	Examiner
714	39	2/6/2006	PFC

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
Inventor Search	2/3/2006	PFC
EAST (all databases searched)	2/13/2006	PFC
709/224 ; 714/4,37,712 ; 326/37,39 ; 710/15 - text searched [see included search report]	2/7/2006	PFC
Google.com(+xbi +analyzer/fpga/diagnostic ; define: backplane,xbi,small form factor ; +"bit file" +"network testing" +fpga)	2/13/2006	PFC
ACM/IEEE (+xbi +fpga)	2/16/2006	PFC
ACM (+programmable +"logic analyzer" +network)	2/17/2006	PFC